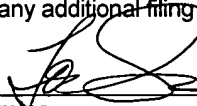




JW

AMENDMENT TRANSMITTAL LETTER				Docket No. R2180.0193/P193	
Application No. 10/801,838-Conf. #3147		Filing Date March 17, 2004		Examiner A. Kalam	
Art Unit 2814					
Applicant(s): Naohiro Ueda					
Invention: SEMICONDUCTOR APPARATUS AND METHOD OF MANUFACTURING THE SAME					
<b>TO THE COMMISSIONER FOR PATENTS</b>					
Transmitted herewith is an amendment in the above-identified application.					
The fee has been calculated and is transmitted as shown below.					
<b>CLAIMS AS AMENDED</b>					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	12	- 20 =	0	x	0
Independent Claims	1	- 3 =	0	x	0
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
<b>TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:</b>					<b>0.00</b>
<input checked="" type="checkbox"/> Large Entity <input type="checkbox"/> Small Entity					
<input checked="" type="checkbox"/> No additional fee is required for this amendment.					
<input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of \$ _____. A duplicate copy of this sheet is enclosed.					
<input type="checkbox"/> A check in the amount of \$ _____ to cover the filing fee is enclosed.					
<input type="checkbox"/> Payment by credit card. Form PTO-2038 is attached.					
<input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. <u>04-1073</u> as described below. A duplicate copy of this sheet is enclosed.					
<input checked="" type="checkbox"/> Credit any overpayment.					
<input checked="" type="checkbox"/> Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.					
 Thomas J. D'Amico Attorney/Agent Reg. No.: 28,371				Dated: <u>January 24, 2007</u>	
DICKSTEIN SHAPIRO LLP 1825 Eye Street, NW Washington, DC 20006-5403 (202) 420-2232					



Docket No.: R2180.0193/P193  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Naohiro Ueda

Application No.: 10/801,838

Confirmation No.: 3147

Filed: March 17, 2004

Art Unit: 2814

For: SEMICONDUCTOR APPARATUS HAVING  
MOS TRANSISTOR, ELECTRODE PAD  
AND ANALOG CIRCUIT (as amended)

Examiner: A. Kalam

**AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INTRODUCTORY COMMENTS**

In response to the Office Action dated October 25, 2006, please amend the above-identified U.S. patent application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper.

**Remarks** begin on page 6 of this paper.